

**SUNDARGARH ENGINEERING SCHOOL, SUNDARGARH**  
**DEPARTMENT OF ELECTRICAL ENGG**

**TIME TABLE FOR 4TH AND 6TH SEMESTER**

DAY	SEM	10.00 TO 10.50		11.40 TO 12.30		12.30 TO 01.20		01.20 TO 02.00		02.00 TO 02.50		02.50 TO 03.40		03.40 TO 04.30	
		TO	TO	TO	TO	TO	TO	TO	TO	TO	TO	TO	TO	TO	TO
MON	4th	EMI (UDAYA)	EC-I (A.K.PATEL)	AEROPAM (SAMBHU)	GTD (S.S.PADHI)	ELECTRICAL LAB PRACTICE-I (GR-A) SIMULATION&MAT LAB (GR-B)(S.PADHI)									
	6th	ELECTRICAL WORKSHOP PRACTICE (GR-A) PROJECTWORK PHASE-II (GR-B) (B.TOPPO)													
TUES	4th	GTD (S.S.PADHI)	ELECTRICAL LAB PRACTICE-I (GR-B)(B.TOPPO) SIMULATION&MAT LAB (GR-A)												
	6th	RE (D-PATEL)	EIE (U.S.BEHERA)	SGPD (S-DAS)	CSE (S.S.PADHI)	ELECTRICAL LAB PRACTICE (GR-A)(B.TOPPO) ANALOG ELECTRONIC LAB (GR-B)									
WED	4th	EC-I (A.K.PATEL)	ELECTRICAL LAB PRACTICE (GR-A)(B.TOPPO) ANALOG ELECTRONIC LAB (GR-B)												
	6th	EIE (U.S.BEHERA)	SGPD (S-DAS)	CSE (S.S.PADHI)	RE (D-PATEL)	ENGINEERING DRAWING (S.DASH)									
THURS	4th	GTD (S.S.PADHI)	ENGINEERING DRAWING (S.DASH)												
	6th	ELECTRICAL WORKSHOP PRACTICE (GR-B) PROJECTWORK PHASE-II (GR-A)(B.TOPPO)													
FRI	4th	EC-I (A.K.PATEL)	ELECTRICAL LAB PRACTICE (GR-B) ANALOG ELECTRONIC LAB (GR-A)(S.NAYAK)												
	6th	RE (D-PATEL)	EIE (U.S.BEHERA)	SGPD (S-DAS)	CSE (S.S.PADHI)	ELECTRICAL LAB PRACTICE (GR-A) PROJECTWORK PHASE-II (GR-B) (B.TOPPO)									
SAT	4th	EMI (UDAYA)	ENGINEERING DRAWING (S.DASH)												
	6th	EIE (U.S.BEHERA)	RE (D-PATEL)	CSE (S.S.PADHI)	SGPD (A.K.PATEL)	STUDENT CENTERED ACTIVITIES (SCA)									

**R E C R E A T I O N**

Signature of HOD  
 15/12/23  
 (Electrical engineering Dep.)

Signature of  
 Sundargarh Engineering School.  
 Sundargarh