## SUNDARGARH ENGINEERING SCHOOL, KIREI LESSON PLAN

Subject : DIGITAL ELECTRONICS AND MICROPROCESSOR

## Discipline : ELECTRICAL ENGINEERING

## Faculty : SHAMBU SINGH

## Semester : 5TH

Week	Weekly classes	Theory Topics
1 <sup>st</sup>	1 <sup>st</sup>	Introduction to DIGITAL ELECTRONICS
	2 <sup>nd</sup>	NUMBER SYSTEMS AND CODES
	3rd	List different number system & their relevance: binary, octal, decimal, Hexadecimal, Study the Conversion from one number system to another
	4 <sup>th</sup>	Perform Arithmetic operations of binary number systems.
2 <sup>nd</sup>	1 <sup>st</sup>	1's & 2's complement of Binary numbers., Perform Subtraction of binary numbers using complementary numbers. Perform multiplication and division of binary numbers.
	2 <sup>nd</sup>	Define concept of Digital Code & its application. Distinguish between weighted & non-weight Code
	3rd	Study Codes: definition, relevance
	4 <sup>th</sup>	Types of code (8-4-2-1, Gray, Excess-3 and importance of parity bit.
3 <sup>rd</sup>	1 <sup>st</sup>	LOGIC GATES
	2 <sup>nd</sup>	Discuss the Basic Logic & representation using electric signals
	3rd	Learn the Basic Logic gates (NOT, OR, AND, NOR, NAND, EX-OR & EXNOR) – Symbol, function, expression, truth table & example IC nos.
	4 <sup>th</sup>	Define Universal Gates with examples & realization of other gate
4 <sup>th</sup>	1 <sup>st</sup>	BOOLEAN ALGEBRA
	2 <sup>nd</sup>	Understand Boolean : constants, variables & functions. Comprehend the Laws of Boolean algebra
	3rd	State and prove Demorgan's Theorems. Represent Logic Expression : SOP & POS forms & conversion
	4 <sup>th</sup>	Simplify the Logic Expression/Functions (Maximum of 4 variables) : using Boolean algebra and Karnaugh's map methods
5 <sup>th</sup>	1 <sup>st</sup>	What is don't care conditions ? Realisation of simplified logic expression using K-Map
	2 <sup>nd</sup>	Realisation of simplified logic expression using gates. Illustrate with examples the above.
	3rd	COMBINATIONAL CIRCUITS
	4 <sup>th</sup>	Define a Combinational Circuit and explain with examples. Arithmetic Circuits (Binary)

6 <sup>th</sup>	1 <sup>st</sup>	Realise function, functional expression, logic circuit, gate level circuit, truth table & applications of Half-adders,
	2 <sup>nd</sup>	Full-adder & full-Subtractor.
		Explain Serial & Parallel address: concept comparison & application
	3rd	Discuss Multiplexers: definition, relevance, gate level circuit of simple. De-multiplexers (1:4) logic circuit with truth Table
	4 <sup>th</sup>	Explain the working of Binary-Decimal Encoder & Decoder.
7 <sup>th</sup>	1 <sup>st</sup>	Working of 2-bit Magnitude Comparator: logic expression, truth table
	2 <sup>nd</sup>	SEQUENTIAL CIRCUITS
	3rd	Define Sequential Circuit : Explain with examples.
	4 <sup>th</sup>	Know the Clock-definition characteristics, types of triggering & waveform.
8 <sup>th</sup>	1 <sup>st</sup>	Define Flip-Flop, Study RS, Clocked RS, D, T, JK, MS-JK flip-flop with logic Circuit and truth tables.
	2 <sup>nd</sup>	
	2	Concept of Racing and how it can be avoided.
	3rd	Applications of flip-flops & its conversion.
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	4 <sup>th</sup>	COUNTERS
9 <sup>th</sup>	1 <sup>st</sup>	List the different types of counters-Synchronous and Asynchronous.
	$2^{nd}$	Explain the modulus of a counter
	3rd	COUNTERS
	4 <sup>th</sup>	List the different types of counters-Synchronous and Asynchronous.
		Explain the modulus of a counter
1 oth	1 st	4-bit asynchronous counter with timing diagram
10 <sup>th</sup>	1 <sup>st</sup>	Asynchronous decade counter
	2 <sup>nd</sup>	4-bit synchronous counter
	3rd	Compare Synchronous and Asynchronous counters and know their ICs nos.
	4 <sup>th</sup>	REGISTERS
11 <sup>th</sup>	1 <sup>st</sup>	Explain the working of various types of shift registers – SISO
	2 <sup>nd</sup>	SIPO
	3rd	PISO
	4 <sup>th</sup>	PIPO, with truth table using flip flop.
12 <sup>th</sup>	1 <sup>st</sup>	8085 MICRO PROCESSOR
	2 <sup>nd</sup>	Introduction to microprocessor, Micro computers

	3rd	Architecture of intel 8085A Microprocessor
	4 <sup>th</sup>	Functional Block diagram and Description of each block.
13 <sup>th</sup>	1 <sup>st</sup>	Pin diagram and description.
	$2^{nd}$	
	3 <sup>rd</sup>	Stack, Stack Pointer, Stack Top
	4 <sup>th</sup>	Interrupts, Op-code & Operands
14th	$1^{st}$	Grouping and Explanation of different group instructions with examples
	2 <sup>nd</sup>	Instruction sets & Addressing modes , Timing diagram of different machine cycle, 8085A timing states.
	3rd	Instruction fetching and execution, Timing diagram of different machine cycle. Basic Interfacing Concept, Memory Mapping & I/O Mapping
	4 <sup>th</sup>	Programmable peripheral interface Intel -8255, Functional block diagram and Operation of 8255, Programming of 8255, Application Using 8255: Seven Segment LED display

HOD, Electrical Department

Principal